

Silterra Editorial Contact Information:

Kevin Bligh
Silterra Malaysia Sdn. Bhd.
Tel: +604-403-3888
Email: kevin_bligh@silterra.com
lfarber@2ndstorymedia.com

Lawren Farber
2nd Story Media
Tel: +1-408-323-1618
Email:

Key ASIC Editorial Contact Information:

Alan P. Aronoff
Key ASIC, Inc.
Tel: +1-408-567-4184

Kevin Metz
Shelton Group
Tel: +1-972-239-5119 x135

E-Mail: aaronoff@keyasic.com

E-Mail: kmetz@sheltongroup.com

Key ASIC and Silterra Partner to Deliver Design-to-Manufacture Services Targeted for Mobile and Consumer Electronics Markets

SANTA CLARA, Calif. and KULIM, Malaysia (May 22, 2006) – Key ASIC, a leading fabless provider of ASIC and System-on-Chip (SoC) design-to-manufacture services for portable and consumer applications, and Silterra Malaysia Sdn. Bhd., a leading semiconductor wafer foundry offering leading foundry matched CMOS logic, high-voltage and mixed-signal/RF technologies, today announced a partnership to offer streamlined design-to-manufacture services for ASICs in the mobile and consumer electronics markets.

As part of the collaboration between the two companies, Key ASIC will provide mutual customers access to its wide IP portfolio and design services on Silterra's 0.18- and 0.13-micron CMOS process technologies. This powerful partnership offers customers a cost effective solution for optimized performance and fast time to market.

"Our study of the market revealed the recent rise of many fabless ASIC companies with foundries and design houses coupling to offer streamlined IDM services," said Kah Yee Eg, founder and CEO of Key ASIC. "Because we have a core competence in low-power and high-performance design and an IP portfolio targeted for consumer applications, Silterra was a natural foundry choice by virtue of its focus in the same market segments and its high-quality silicon process technologies."

Key ASIC will provide customers with its KeySoC™ Platform, which provides a high-performance, ARM-based CPU platform and memory blocks for integration of domain-specific IP blocks to design ASICs quickly.

Key ASIC's silicon-proven IP portfolio for portable consumer devices includes a wide variety of mixed-signal blocks and peripheral interfaces for quick integration in applications including PMP/portable games, MP3/digital audio players, digital imaging/video, mobile phones and wireless networking.

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Silterra offers CMOS logic and mixed-signal process technologies down to 0.13-micron. As a leading provider of processes for small form factor color display drivers and power management devices, Silterra also offers a portfolio of high-density, high-voltage technologies in 0.20- and 0.18-micron nodes for these high-growth applications.

“Silterra has built a network of very capable design service partners to support our growing customer base,” said Bruce Gray, CEO of Silterra. “As a member of the Silterra Design Community, Key ASIC brings a lot of value to our customers by complementing our process and manufacturing capabilities with their design expertise to jointly provide customers with top-quality silicon through a seamless turnkey solution. This is an attractive approach for customers to speed up their time-to-revenue schedule very cost effectively.”

About Silterra Malaysia Sdn. Bhd.:

Silterra Malaysia Sdn. Bhd. is a leading semiconductor wafer foundry that provides advanced foundry standard CMOS logic, high-voltage and mixed-signal/RF technologies. The company's wafer fab has a designed capacity of 40,000 eight-inch wafers per month. Silterra, which is committed to world-class service and environmental friendliness, received Notable Mention in the Malaysian Prime Minister's Hibiscus Award competition for Environmental Performance in 2003 and 2005. The company is ISO 9001:2000 and ISO 14001 certified. Silterra's headquarters and factory are located in Kulim, Malaysia, and has offices in Kuala Lumpur (Malaysia), San Jose (California, USA) and Hsinchu (Taiwan). For additional information on Silterra or its services, please visit www.silterra.com.

About Key ASIC:

Key ASIC is a privately held fabless provider of high-performance, low-power ASIC design and manufacturing services for consumer, wireless and personal electronics applications. With proven silicon and a broad, domain-specific IP platform ranging from digital and mixed-signal component IP blocks to peripheral IP blocks, Key ASIC offers superior design flow and optimization technology to quickly and cost-effectively meet system-on-chip (SoC) requirements for high-volume, high-performance, and low cost consumer applications. Key ASIC's design expertise and unique IP address critical area, integration and cost challenges for mixed-signal, RF, low-power and high-voltage technologies. Design locations in Silicon Valley (USA) and Malaysia, combined with extensive manufacturing and logistics resources and a flexible engagement model, provide Key ASIC customers with a comprehensive support system for modular ASIC innovation from IP development through prototype to production.

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